

Abstract of the Disclosure

A dual packet configuration for wireless communications including a first portion that is modulated according to a serial modulation and a second portion that is modulated according to a parallel modulation. The serial modulation may be DSSS whereas the parallel modulation may be OFDM. The first portion may include a header, which may further include an OFDM mode bit and a length field indicating the duration the second portion. The first portion may be in accordance with 802.11b to enable dual mode devices to coexist and communicate in the same area as standard 802.11b devices. The dual mode devices can communicate at different or higher data rates without interruption from the 802.11b devices. The packet configuration may include an OFDM signal symbol which further includes a data rate section and a data count section. In this manner, data rates the same as or similar to the 802.11a data rates may be specified between dual mode devices. The first and second portions may be based on the same or different clock fundamentals. For OFDM, the number of subcarriers, pilot tones and guard interval samples may be modified independently or in combination to achieve various embodiments. Also, data subcarriers may be discarded and replaced with pilot tones for transmission. The receiver regenerates the discarded data based on received data, such as using ECC techniques.